SM2246EN Flash F/W & ISP Release Information – P1228BS

Introduction
This purpose of this document is to provide release information on the SM2246EN F/W and ISP release information

Fix Coverage
■   stands for the **"new fix"** or **"new support"** in the category
□ stands for the **"no-update"** in the category

■ **Tester FW**	■ **Controller ISP**
□ **Yield Issue**	■ **Yield Issue**
■ **Flash Issue**	■ **Flash Issue**
□ SLC Flash	□ SLC Flash
□ Samsung Flash	□ Samsung Flash
□ Toshiba/Sandisk Flash	□ Toshiba/Sandisk Flash
□ Intel/Micron Flash	□ Intel/Micron Flash
□ Hynix Flash	□ Hynix Flash
□ Others	□ Others
■ MLC Flash	■ MLC Flash
■ Samsung Flash	■ Samsung Flash
■ Toshiba/Sandisk Flash	■ Toshiba/Sandisk Flash
■ Intel/Micron Flash	■ Intel/Micron Flash
■ Hynix Flash	■ Hynix Flash
□ **Compatibility issue**	□ **Compatibility issue**
□ **Tester Bug Fix**	■ **ISP Bug Fix**
■ **AP Bug Fix & New Function**	□ **Feature Enhance**
■ **Feature Enhance**	

ISP Revision History

Version	MP Tool version	ISP version	Note
P1228BS	Q0203A	P1228BS	**This FW supports Intel/Micron L06B 3D MLC NAND (featuring SLC caching)** 1. **Update Security FW to version: Q0126A.** 2. **Support L06B whole drive SLC mode(except for system blocks).** 3. **Support flush dram as SPOR feature for 3.3V devices.** 4. **Reduce command latency as GC is under operation.** 5. **Reduce time consumption on dram access in update/check map functions.** 6. **Return COMRESET status in ISR if COMRESET occurs in Trim.** 7. **Add pretest error code: 0x29 to sort power-on one pass L06B.** 8. **Add RDT features: Read retry could be enabled in RDT by MP tool.** 9. **Add max/min Temp. in SMART ID: 0xC2(optional setting in MP tool).** 10. **Issue fix: Modify the cases that FW would clear command as LSCS occurs.** 11. **Issue fix: As enable/disable write cache is set by SCT command, the feature can not be changed by set feature command. Device should not return error as well.** 12. **Issue fix: Under force enable write cache, device should not return error as host disable it.**
P0819AS	P0810A	P0819AS	This FW is the first release of SLC caching FW. 1. Only supports Intel/Micron L06B. 2. Please check the corresponded RDK for PCB modification, especially the extra capacitors which will be used for LPP (Lower Page Protection) 3. For the configuration of 4ch/4CE or 4ch/8CE, normal FW (non-SLC caching) is suggested instead of SLC caching FW.

Note:

1. F/W and ISP update is recommended.
2. History # is denoted by "Version-Date" .

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