



Silicon Motion, Inc.

SM2242 Test Program/ ISP Release Note

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**SM2242 Test Program and ISP Release Note:**

Release Date	ISP Version	ISP Check Sum	Test AP Version	Description
2009/07/09	AB:2009-07-03	006F989E	2009-07-03	SM2242 formal release: <ol style="list-style-type: none">1. Add flash I/O setting function2. Enable write cache
2009-09-11	AB: 2009-08-31	0070A6C0	2009-09-08	SM2242 formal release: <ol style="list-style-type: none">1. Add device capacity adjustment.2. Add 48 bit feature set.3. Support HPA feature set.4. Add HW "write protect" on P40, and Quick Erase on P41. Enable this feature by MP Tool.
2009-10-16	AB: 2009-10-09	007187BF	2009-10-09	SM2242 new update features: <ol style="list-style-type: none">1. Support ONFI IM Flash without ONFI table (34nm SLC and MLC).2. Modify SMART command Read Data (0xD0) to add Total Bad count, New Bad count and CRC error counter.3. Fix some info can't be update in SSD Life Guard.4. Extend average erase count for SLC Flash.5. Modify SMART command Read Data (0xD0): Only record CRC error count during one power cycle.6. Abort CFA ERASE SECTORS.



				<ul style="list-style-type: none">7. Fix CFA TRANSLATE SECTOR command.8. Report SERVICE and PACKAGE CMD
2009-11-27	AB:2009-11-06	006FF992	2009-11-27	<p>SM2242 new update features:</p> <ul style="list-style-type: none">1. Support internal interleave for SAMSUNG flash.2. Disable LBA48 when device size small than 8G Bytes avoid host compatibility issue.3. Report support TRIM command and non-rotate device.4. Report not support read write log.5. Modify LED Pin define. P40:LED for H/W WP, P42: LED for SATA PHY.6. Support runtime detect HW write protect.7. Hold at least 1s to active HW quick erase.8. Modify report spare block from run time spare block to total spare block (include child block) in READ DATA (0xD0) of SMART command.
2010/2/11	AB: 2010/02/08	00725057	2010/02/08	<p>SM2242 new update features:</p> <ul style="list-style-type: none">1. Support Hynix H27UDGV5ATR, H27UCGV5ATR.2. When read ECC fail, issue one plane read command to retry for 2 plane read flash.3. Support Toshiba TC58DVG3S0ETA00, TH58DVG5S0ETA20 and TH58DVG4S0ETA20.



				<ul style="list-style-type: none">4. Fix Samsung K9K8G, K9KAG read data issue in internal interleave mode.5. Enable link table only use strong page in MLC Flash.6. Add ISP Block Error Handle (Load ISP code with ECC failure, assign another ISP block to current ISP Block).7. Disable Hybrid mode.
2010-03-11	AB: 2010-02-22	007256F6	2010-02-22	<p>SM2242 FW change:</p> <ul style="list-style-type: none">1. Fix get status command issue on Hynix 32nm.2. Check minimum spare block after adjusting capacity when initial card.3. Modify controller to flash driving strength to be 3 level bases on flash number.4. Add Fix Toshiba ID shift by Database.